

JumpGen Introduces TILEPro64 Processor AdvancedMC Board

The TPM-100 features the Tiler TILEPro64 processor and expands JumpGen's PrAMC line

Carlsbad, Calif., May, 12, 2009 – JumpGen Systems today announced the TPM-100, an AdvancedMC™ (PrAMC) platform based on the Tiler® TILEPro64™ processor that offers 64 cores of general purpose and signal processing compute coupled with over 20 Gbps of full-duplex I/O. The TPM-100 PrAMC offers new levels of performance enabled by the new TILEPro64 processor design features and the Tiler Multi-Core Development Environment (MDE) version 2.0.

The single-wide TPM-100 is the latest JumpGen PrAMC platform that supports 10 Gbps Ethernet interfaces to address growing market requirements for IP networks. The TPM-100 and T6M-100 AMCs provide equipment manufacturers with options to dramatically reduce time-to-market and fulfill the growing demand for packet processing in converged, content-aware IP networks that process data, voice and video. The board's dual 10Gbps Ethernet interfaces and 64 processor cores make it ideal for hosting high-bandwidth embedded communication applications such as wireless radio network controllers (RNCs), multimedia gateways, media transcoders/transraters, session border controllers, modular servers, firewalls, and deep-packet inspection appliances.

"The TPM-100 is an excellent implementation of the TILEPro64 processor together with MDE version 2.0; it continues to raise the bar by offering the highest performance and performance-per-watt of any AMC card on the market today. This further establishes the Tile processors leadership in the embedded market," said Omid Tahernia, CEO and President, Tiler Corporation. "JumpGen is a valued embedded board developer and partner."

"We are pleased with the new Tiler MDE version 2.0 features including support for the new JumpGen PrAMC product line. Our customers have reported performance gains based upon the new capabilities included in MDE version 2.0 using the TILE64 processor." said Harry White, JumpGen Systems President and CEO. "The TPM-100 provides even higher performance and lower power options to our customers."

"The TPM-100 processor board from JumpGen Systems is a high performance product expanding the AMC ecosystem and JumpGen's growing PrAMC product line. The TPM-100 will enable higher performance MicroTCA® and ATCA System options for a wide range of telecom, military/defense, and industrial applications." – Lance A. Leventhal, Technology Editor, ATCA Newsletter.

"JumpGen Systems continues to be a leader in AdvancedMC product development and is expanding the ecosystem for AMC.0 R2.0 and AMC.2 R1.0 compliant products. We are pleased that JumpGen Systems is developing new products based on many of the PICMG standards," said Joe Pavlat, President of PICMG, the open standards consortium.

Application development on the TPM-100 is supported by the Tiler Multicore Development Environment (MDE) version 2.0. MDE 2.0 includes new features like Zero Overhead Linux (ZOL), which enables dataplane applications to run on Linux while avoiding all of Linux's overhead; and Bare Metal Environment (BME), which is suitable for RTOS porting and low-level dataplane applications. The MDE also offers an integrated set of tools and libraries that brings simplicity to multicore programming and helps programmers harness the full potential of their processors. It also provides full support for C/C++, standard programming paradigms, and the most advanced multicore debugging and optimization tools.

TPM-100 Features

- TILEPro64 processor with 64 cores running at 700MHz
- 2GB of ECC DDR2 memory running at 800MHz
- Up to 8GB of persistent memory (solid state drive)
- Dual 10GigE interfaces (AMC.2 Type 6 or AMC.2 Type 5 with 10Gbps SFP+ on front panel)
- Dual GigE interfaces (AMC.2 Type E2)
- Front Panel I/O includes an RS-232 Serial and an optional SFP+ for 10Gbps fiber connection

- Available in both full and mid-size AMC configurations for AdvancedTCA® (ATCA), MicroTCA® and proprietary architecture systems.
- RoHS-compliant
- Complete set of tools and runtime software stack provided by the Tilera Multicore Development Environment (MDE version 2.0)

Availability

The TPM-100 is shipping to select customers now and will be generally available in Q3, 2009.

About JumpGen Systems:

JumpGen Systems is an agile, innovative supplier of embedded computing solutions. JumpGen Systems features an experienced team of engineers to define, develop and deliver critical embedded computing products for our customers. JumpGen's team has a successful track record in bringing new embedded processor solutions to market, quickly and cost-effectively. The JumpGen Systems team has delivered production solutions to embedded customers in industry standard form factors such as AdvancedMC, AdvancedTCA, CompactPCI®, PMC, XMC, and VME form factors as well as custom, proprietary solutions. JumpGen Systems is a privately held company headquartered in Carlsbad, California. JumpGen Systems is an Executive Member of PICMG®. For more information, visit www.JumpGen.com

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